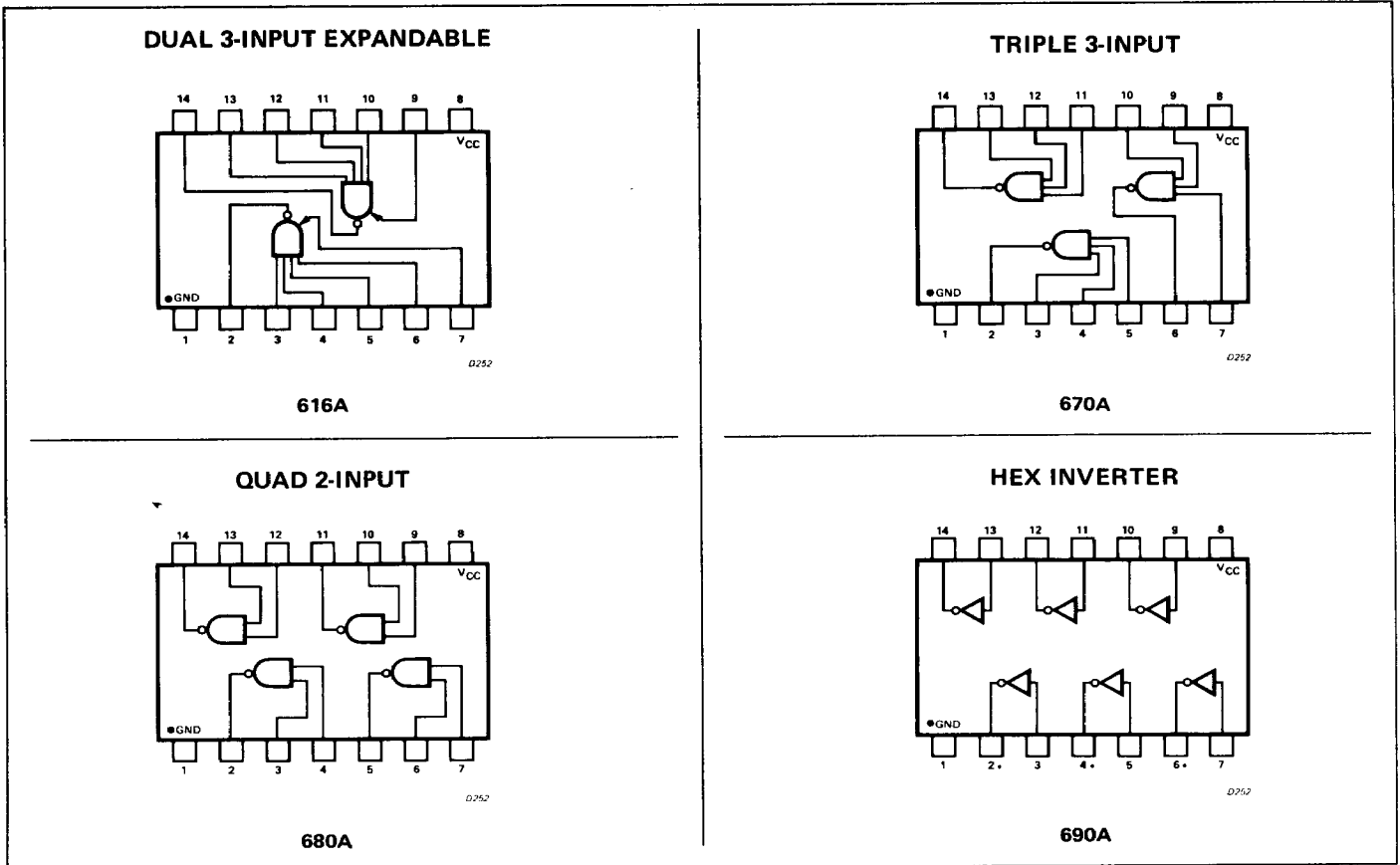


PIN CONFIGURATION



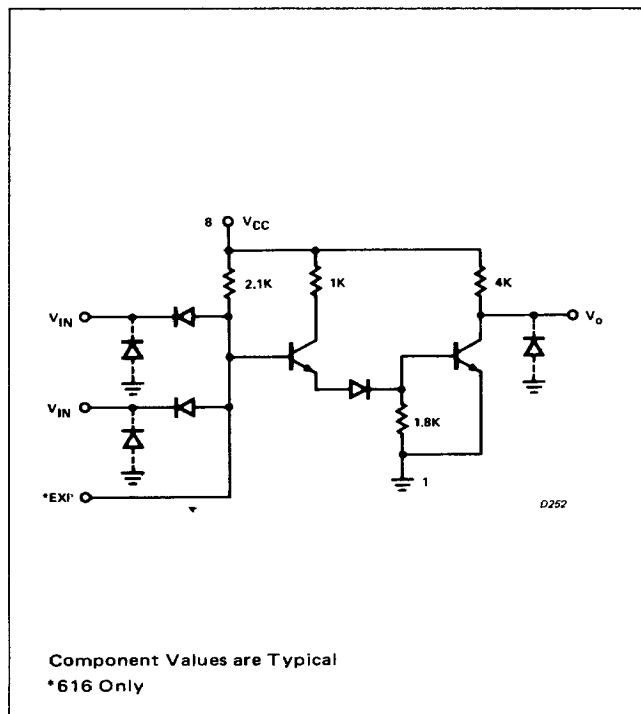
ELECTRICAL CHARACTERISTICS (Notes 1, 2, 3, 5 and 7)

Standard Conditions: $V_{CC} = 5.0V$, $T_A =$ Operating Temp. Range (Unless Noted)

CHARACTERISTIC	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Noise Immunity for "1"	See Note 6	800	1200		mV
Noise Immunity for "0"	See Note 6	300	600		mV
Output Voltage "1" Level	$V_{in} = 0.9V$, $I_{out} = 260\mu A$	3.5			V
Output Voltage "0" Level	$V_{in} = 2.7V$, $I_{out} = 20mA$			0.6	V
	$V_{in} = 2.1V$, $I_{out} = 12.5mA$			0.4	V
Input Current input high	$V_{in} = 5.0V$			25	μA
Input Current input low	$V_{in} = 0.6V$			-2.5	mA
Input Current input low (expander)	$V_{in} = 1.1V$			-2.5	mA
Power Supply Current output high	$V_{in} = 0V$, $T_A = 25^\circ C$			2.8	mA/gate
Power Supply Current output low	$V_{in} = 4.0V$, $T_A = 25^\circ C$			9.0	mA/gate
Turn on Delay	See Test Figure 1, $T_A = 25^\circ C$		25	65	ns
Turn off Delay	See Test Figure 1, $T_A = 25^\circ C$		40	70	ns
Fan-out					
-To sink loads (2.5mA/load)				8	
-To source loads (180 μA /load)				1	

Typical Values are for $T_A = 25^\circ C$. See Page 3 for Notes.

SCHEMATIC DIAGRAM



TEST CIRCUIT AND WAVEFORM

